Amendments to the Claims

1. (Currently Amended) A power sequence apparatus comprising:

a power supply for simultaneously generating a gate high voltage and a gate low voltage;

a device driving circuit ~~for~~ sequentially ~~outputting~~ ~~supplying~~ the gate high voltage and then the gate low voltage ~~to a device~~; and

a voltage controller, disposed between the power supply and the device driving circuit, for simultaneously receiving the gate high voltage and the gate low voltage from the power supply, and processing the gate high voltage using a plurality of switching circuits to supply the gate high voltage ~~to the device driving circuit~~ after supplying the gate low voltage ~~is supplied~~ to the device driving circuit.

2. (Original) The power sequence apparatus according to claim 1, wherein the voltage controller includes:

a first switching circuit disposed between the power supply and the device driving circuit to switch to the device driving circuit the gate high voltage that is output from the power supply; and

a second switching circuit connected between the first switching circuit and a gate low voltage output line of the power supply to control a switching point of the first switching circuit.

3. (Original) The power sequence apparatus according to claim 2, wherein the voltage controller further includes:

a first resistor and a capacitor connected in parallel between the second switching circuit and the gate low voltage output line for switching the second switching circuit according to a RC time constant of the first resistor and the capacitor; and

a second resistor connected between the second switching circuit and a ground voltage source for discharging a charged voltage of the capacitor to the ground voltage source.

4. (Original) The power sequence apparatus according to claim 2, wherein the voltage controller further includes:

a RC circuit disposed between the second switching circuit and the gate low voltage output line of the power supply.

5. (Original) The power sequence apparatus according to claim 2, wherein the first switching circuit and the second switching circuit are integrated into a single chip.

6. (Original) The power sequence apparatus according to claim 2, further comprising:

a current control resistor connected between the first switching circuit and the second switching circuit for controlling a switching speed of the first switching circuit.

7. (Original) The power sequence apparatus according to claim 2, wherein at least one of the first and second switching circuits includes a transistor.

8. (Currently Amended) A power sequence apparatus comprising:

a power supply for simultaneously generating a gate high voltage and a gate low voltage;

a device driving circuit ~~for~~ sequentially ~~outputting~~ ~~supplying~~ the gate high voltage and the gate low voltage ~~to a device~~;

a switching part, disposed between the power supply and the device driving circuit, for simultaneously receiving the gate high voltage and the gate low voltage ~~from the power supply,~~ and switching the gate high voltage to supply the gate high voltage to the device driving circuit after ~~supplying~~ the gate low voltage ~~is supplied~~ to the device driving circuit; and

a timing control part generating a switching control signal to the switching part to control a switching action of the switching part.

9. (Original) The power sequence apparatus according to claim 8, wherein the switching part includes:

a first switching circuit connected between the power supply and the device driving circuit for switching to the device driving circuit the gate high voltage output from the power supply; and

a second switching circuit connected between the first switching circuit and a gate low voltage output line of the power supply for controlling a switching point of the first switching circuit according to the switching control signal from the timing control part.

10. (Original) The power sequence apparatus according to claim 9, wherein at least one of the first and second switching circuits includes a transistor.

11. (Original) The power sequence apparatus according to claim 8, wherein the timing control part supplies the switching control signal to the switching part after a driving power is supplied to the power supply and after the gate low voltage is supplied from the power supply to the device driving circuit.

12. (Previously Presented) A display device comprising:

a display panel for displaying an image;

a data driver for driving the display panel;

a power supply for simultaneously generating a gate high voltage and a gate low voltage;

a voltage controller for simultaneously receiving the gate high voltage and the gate low voltage and sequentially supplying the gate high voltage and the gate low voltage to a gate driver using first and second switching circuits; and

the gate driver for sequentially supplying the gate high and low voltages output from the voltage controller to gate lines of the display panel.

13. (Original) The display device according to claim 12, wherein the voltage controller includes:

the first switching circuit disposed between the power supply and the gate driver to switch to the gate driver the gate high voltage that is output from the power supply; and

the second switching circuit connected between the first switching circuit and a gate low voltage output line of the power supply to control a switching point of the first switching circuit.

14. (Original) The display device according to claim 13, wherein the

voltage controller further includes:

a first resistor and a capacitor connected in parallel between the second switching circuit and the gate low voltage output line for switching the second switching circuit according to a RC time constant of the first resistor and the capacitor; and

a second resistor connected between the second switching circuit and a ground voltage source for discharging a charged voltage of the capacitor to the ground voltage source.

15. (Original) The display device according to claim 12, further comprising:

a current control resistor connected between the first switching circuit and the second switching circuit for controlling a switching speed of the first switching circuit.

16. (Original) The display device according to claim 12, wherein at least one of the first and second switching circuits includes a transistor.

17. (Currently Amended) A display device comprising:

a display panel for displaying an image;

a data driver for driving the display panel;

a power supply for simultaneously generating a gate high voltage and a gate low voltage;

a gate driving circuit sequentially supplying the gate high voltage and the gate low voltage to gate lines of the display panel;

a switching part, disposed between the power supply and the gate driving circuit, for simultaneously receiving the gate high voltage and the gate low voltage and switching the gate high voltage to supply the gate high voltage to the gate driving circuit after supplying the gate low voltage ~~is supplied~~ to the gate driving circuit; and

a timing control part generating a switching control signal to the switching part to control a switching action of the switching part.

18. (Original) The display device according to claim 17, wherein the switching part includes:

a first switching circuit connected between the power supply and the gate driving circuit for switching to the gate driving circuit the gate high voltage output from the power supply; and

a second switching circuit connected between the first switching circuit and a gate low voltage output line of the power supply for controlling a switching point of the first switching circuit according to the switching control signal from the timing control part.

19. (Original) The display device according to claim 18, wherein at least one of the first and second switching circuits includes a transistor.

20. (Currently Amended) A method of driving a power sequence apparatus, comprising the steps of:

generating a gate high voltage and a gate low voltage;

simultaneously receiving the gate high voltage and the gate low voltage and supplying the gate high voltage to a gate driving circuit by using a plurality of switching circuits to switch the gate high voltage after supplying the gate low voltage ~~is supplied~~ to the gate driving circuit; and

sequentially supplying the gate low voltage and the gate high voltage to a plurality of electrodes.